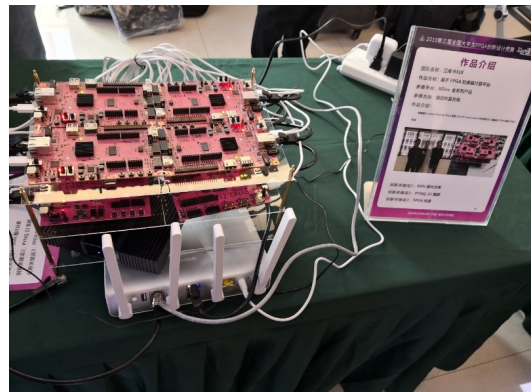


基于FPGA集群的类脑计算平台

Neuromorphic computing platform based on PYNQ cluster

> Motivation

- >> Implementation of unsupervised learning of image classification through spiking neuron network
- >> Explore the design of Neuromorphic computing which large-scale and low-power systems through software/hardware co-design
- >> Promote more people to develop FPGA-based Neuromorphic architecture research and learning through open source



> Introduction

- >> Spiking neuron network simulator NEST
- >> SNN image classification
- >> Neuron computing and STDP accelerator base on FPGA
- >> MPI communication between PYNQ

