

An Ultra-fast NoC Simulator based on Zynq

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Design

- This NoC Simulator is suitable for mesh-based NoC.
- Apply the combination FPGA and ARM to implement the NoC simulation.
- Use configurable prototype of standard 5-stage NoC routers to replace the model of NoC router to improve the accuracy and simulation speed.

Results

- The simulator can be used to evaluate the performance of NoC router, the effectiveness of routing algorithms and NoC mapping results.
- Achieve more than 100x speed-up compared to software-based NoC simulators.

